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AF/2815

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
**BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re application of:

Ki-Young LEE, et al.

Serial No. 09/389,491

Filed: September 3, 1999

For: Semiconductor Integrated Circuit  
Capacitor and Method for  
Fabricating Same

Examiner: Paul E. Brock II

Group Art Unit: 2815

**RECEIVED**

**FEB 23 2004**

**RESPONSE TO NOTICE OF NON-COMPLIANCE**

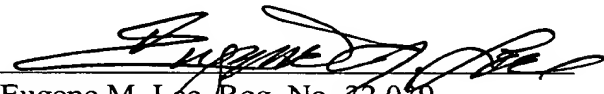
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA. 22313-1450

In response to the Notice of Non-compliance dated January 15, 2004, attached hereto is the complete Appellant's Brief under 37 C.F.R. 1.192 together with Appendix A.

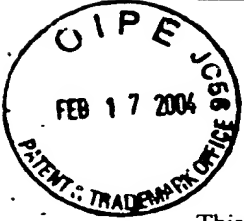
Respectfully submitted,

LEE & STERBA, P.C.

Date: February 17, 2004

  
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**PETITION and  
DEPOSIT ACCOUNT CHARGE AUTHORIZATION**

This document and any concurrently filed papers are believed to be timely. Should any extension of the term be required, applicant hereby petitions the Director for such extension and requests that any applicable petition fee be charged to Deposit Account No. 50-1645.

If fee payment is enclosed, this amount is believed to be correct. However, the Director is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-1645.

Any additional fee(s) necessary to effect the proper and timely filing of the accompanying papers may also be charged to Deposit Account No. 50-1645.

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**FEB 23 2004**